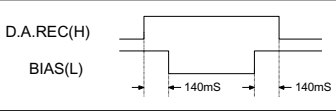


31	20MHz.IN	I	Main oscillator input	---	STOP	STOP	---
32	20MHz.OUT	O	Main oscillator output	---	STOP	STOP	---
33	+5V(D)	I	5V input terminal	---	---	---	---
34	32KHz IN	I	Sub oscillator input	---	---	---	---
35	32KHz OUT	O	Sub oscillator output	---	---	---	---
36	NC(VCONT)	I	Video Control	---	---	---	---
37	V EE(L)	O	Output terminal for switch between EE and VV.	Low	Low	Low	Low
38	D REC(H)	O	Output terminal for video recording current control.	Low	Low	Low	Low
39	D.A.REC(H)	O	Control terminal for normal audio recording current.	Low	Low	Low	Low
40	BIAS(L)	O	Bias oscillation Control terminal for Linear audio recording current. 	High	Hi-Z	Hi-Z	High
41	FM.MUTE(H)	O	Output terminal for audio mute control.	High	High	Low	High
42	N.C	--	(No Connection.)	---	---	---	---
43	POWER_KEY	I	Input terminal for the unit power button. It switches ON to OFF and/or OFF to ON when down edge is detected.	In	In	In	In
44	POS.SW3	I	Input terminal for mecha position.	In	In	In	In
45	POS.SW2	I	Input terminal for mecha position.	In	In	In	In
46	POS.SW1	I	Input terminal for mecha position.	In	In	In	In
47	POWER OFF(H)	O	ON/OFF Control terminal for power circuit. Power off condition: "High" is existed. ("Low" is existed when the mechanism and/or circuit operation is activated.)	High	High	Low	High
48	FIP(L)	O	Output terminal for FIP on/off control signal. ("Low" is existed when the power off condition under the "FIP OFF" setting.)	Not fix	High	Low	Low
49	P50 IN	I	Input terminal for Project 50 serial signal.	In	In	In	In
50	N.C	--	(No Connection.)	---	---	---	---
51	N.C	--	(No Connection.)	---	---	---	---
52	RESET(L)	I	Input terminal for microprocessor reset.	In	In	In	In
53	4FC.LPF	I	4fc clock input terminal	---	---	---	---
54	OSD.FSC IN	I	OSD clock input terminal	---	---	---	---
55	5V(OSD)	I	5V input terminal	---	---	---	---
56	CVIN	I	Input terminal for Composite video signal.	---	---	---	---
57	GND(OSD)	I	GND	---	---	---	---
58	LECHA	I	Input terminal for Composite video signal.(White level)	---	---	---	---
59	20M.START(H)	I	Switching terminal for stertting Mode.(High speed/Low speed)	---	---	---	---
60	MD0	I	Mode setting terminal for flash over-write.	---	---	---	---
61	CVOUT	O	Output terminal for CG Video.	---	---	---	---
62	HLF	O	LPF connecting terminal for the slicer. (For OSD dot clock.)	---	---	---	---
63	SECAM.V.IN	I	Chrominance input terminal for SECAM super impose.	---	---	---	---
64	GND	I	Connect to GND	In	In	In	In
65	S TAB(L)	I	Input terminal for the prevention tab. [EXIST] :LOW [NO EXIST] :HIGH	In	In	In	In
66	UNLOADING(H)	O	Control terminal for loading motor. [Reverse rotation / brake] : "High"	Low	Low	Low	Low
67	LOADING(H)	O	Control terminal for loading motor. [forward rotation / brake] : "High"	Low	Low	Low	Low
68	C.SYNC	I	Input terminal for composite V-sync signal.	In	In	In	In
69	EX.FF/REW(L)	O	Filter control terminal for PB-CTL signal in FF/REW.	Low	Low	Low	Low
70	PAL-/BG/DK(SYS4)	O	Output terminal for TV broadcast system switching signal for video circuit control.	Not fix	Low	Low	Low
71	SECAM/PAL(SYS2)	O	Output terminal for TV broadcast system switching signal for video circuit control.	Not fix	Low	Low	Low
72	SLEEP(L)	O	Output terminal for super power save mode setting. P-OFF in the super power save mode "Low".	High	Low	Low	High
73	PB60Hz(H)	O	Switching terminal for NTSC FM Audio trap filter in PB. [VV (PB,Trick_PB/A-DUB, A-DUB-PS) mode and CTL is 60Hz] : "High" [Other than above condition] : "Low"	Low	Low	Low	Low
74	N.C	--	(No Connection.)	---	---	---	---
75	N.C	--	(No Connection.)	---	---	---	---
76	P FAIL(L)	I	Input terminal for Interrupt signal for power fail detection.	In	In	In	In
77	IR	I	Input terminal for Interrupt signal from IR sensor emitted by Remo-Con.	In	In	In	In
78	P50 OUT	O	Output terminal for Project 50 serial data.	Not fix	Low	Low	Low
79	CAP.ET	O	Output terminal for Capstan Torque command.	0V	0V	Low	0V
80	CYL.ET	O	Output terminal for Cylinder Torque command.	4.213V	4.213V	Low	4.213V
81	N.C	--	(No Connection.)	---	---	---	---
82	HALF_WAVE(H)	O	Switching terminal for capstan driver speed mode.(FAST/SLOW)	In	In	In	In
83	CAP.R/F	O	Switching terminal for capstan motor rotating direction.	Low	Low	Low	Low
84	125Hz	O	125Hz pulse output terminal for clock adjustment.	Not fix	Low	Low	Low
85	SHORT_DN	I	Short Circuit detection terminal for oscillation circuit in REC. *When the "BIAS(H)" is "Low" and this terminal is "High", "F08" is displayed. *When the "BIAS(H)" is "High" and this terminal is "low", "F07" is displayed.	In	In	In	In